

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICATION FOR LETTERS PATENT

**Computer Graphics Methods and Apparatus For Ray
Intersection**

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ATTORNEY'S DOCKET NO. MS1-448US

TECHNICAL FIELD

This invention relates to computer graphics techniques and, more particularly, to apparatus and methods that optimize processing techniques to determine which of a number of shapes that approximate an object, and in particular approximate the surface of an object, have been intersected by a cast ray.

BACKGROUND

Computer graphics techniques typically involve manipulating computer-generated objects. Many techniques have evolved to assist in object manipulation, one of which typically represents or approximates an object and, in particular, approximates the surface of an object as collections of other smaller objects like polygons, e.g. triangles. That is, objects, some of which might have fairly complex surface characteristics, can be represented as collections of triangles that collectively represent or approximate the surface characteristics of the object. When object manipulation takes place, the manipulation operations are performed relative to the smaller shapes that approximate the surface of the object.

One technique that is used to manipulate objects is ray intersection. Ray intersection techniques can be used in a number of different scenarios that include image rendering, object selection, and surface interrogation (i.e. surface querying).

In image rendering, a ray can be directed from a visual point toward a picture element on a screen. Each object that is hit, or intersected, by the ray is set as an object to be drawn on the picture element to which the color of the object is allocated. This method of color processing the object can render effects of optical attributes, such as the reflection or the refraction of the object on the color of the object drawn on the screen by using the colors of the reflected ray from another

background information can be found in the following U.S. Patents: 5,594,844, 5,933,146, 5,313,568, 5,091,960, and 5,138,699.

Accordingly, this invention arose out of concerns associated with improving the apparatus and methods that are used in connection with computer graphics. In particular, the invention arose out of concerns associated with improving object-intersection processing techniques.

SUMMARY

Ray-intersection methods and apparatus that greatly facilitate computer graphics processing are described. In the described embodiment, a collection of shapes is first defined that approximates an object. The described shapes are polygons, with exemplary polygons comprising triangles. Various topologies can be used including triangle meshes, triangle strips, and triangle fans. A ray is cast toward the approximated object. A reference object which, in the illustrated example comprises a plane, is defined to contain the ray. Advantageously, the plane is selected to be parallel to one of the x , y , and z axes which assists in reducing computational complexity.

With the plane and ray having been defined, aspects of the individual shapes are pre-characterized to provide characteristic data. In the illustrated example, pre-characterization takes place by testing each of the vertices of the polygons that make up the approximated object to ascertain their position relative to the reference object. With all of the vertices having been pre-characterized, the characteristic data is used to ascertain the position of the shapes relative to the reference object. This defines a sub-set of shapes that might be intersected by the ray. The sub-set of shapes is then evaluated to ascertain which of the shapes is

1 intersected by the ray. In other embodiments, the reference object can comprise
2 more than one plane, e.g. two or more planes. Thus, each of the individual shapes
3 that comprise a particular approximated object need not be individually tested for
4 an intersection with the ray. Rather, only a sub-set of shapes that might be
5 intersected by the ray are tested, with the other shapes having been ruled out based
6 upon analysis of the characteristic data.

7 8 **BRIEF DESCRIPTION OF THE DRAWINGS**

9 Fig. 1 is an exemplary computer system that can be used to implement the
10 described embodiment of the present invention.

11 Fig. 2 is a flow diagram that describes steps in a method in accordance with
12 the described embodiment.

13 Fig. 3 is a diagrammatic representation of a ray, a reference object and a
14 shape collection in accordance with the described embodiment of the present
15 invention.

16 Fig. 4 is a diagram that illustrates an exemplary polygon topology known as
17 a triangle mesh.

18 Fig. 5 is a diagram that illustrates an exemplary polygon topology known as
19 a triangle strip.

20 Fig. 6 is a diagram that illustrates an exemplary polygon topology known as
21 a triangle fan.

22 Fig. 7 is a diagram that illustrates the polygon topology of Fig. 4
23 undergoing processing in accordance with the described embodiment of the
24 present invention.

1 Fig. 8 is a diagram that illustrates the polygon topology of Fig. 7
2 undergoing processing in accordance with the described embodiment.

3 Fig. 9 is a diagram that illustrates the polygon topology of Fig. 8
4 undergoing processing in accordance with the described embodiment.

5 Fig. 10 is a diagram that illustrates the polygon topology of Fig. 9
6 undergoing processing in accordance with the described embodiment.

7 Fig. 11 is a diagram that illustrates the polygon topology of Fig. 4
8 undergoing processing in accordance with a described embodiment that is different
9 from the embodiment of Figs. 7-10.

10 Fig. 12 is a diagram that illustrates the polygon topology of Fig. 11
11 undergoing processing in accordance with the described embodiment.

12 13 **DETAILED DESCRIPTION**

14 **Exemplary Computer System**

15 Fig. 1 shows a general example of a computer 130 used to implement a
16 computer graphic system in accordance with the described embodiment of the
17 present invention.

18 Computer 130 includes one or more processors or processing units 132, a
19 system memory 134, and a bus 136 that couples various system components
20 including the system memory 134 to processors 132. Bus 136 represents one or
21 more of any of several types of bus structures, including a memory bus or memory
22 controller, a peripheral bus, an accelerated graphics port, and a processor or local
23 bus using any of a variety of bus architectures. System memory 134 includes read
24 only memory (ROM) 138 and random access memory (RAM) 140. A basic
25 input/output system (BIOS) 142, containing the basic routines that help to transfer

1 information between elements within computer 130, such as during start-up, is
2 stored in ROM 138.

3 Computer 130 further includes a hard disk drive 144 for reading from and
4 writing to a hard disk (not shown); a magnetic disk drive 146 for reading from and
5 writing to a removable magnetic disk 148; and an optical disk drive 150 for
6 reading from or writing to a removable optical disk 152 such as a CD ROM or
7 other optical media. Hard disk drive 144, magnetic disk drive 146, and optical
8 disk drive 150 are connected to bus 136 by an SCSI interface 154 or some other
9 appropriate interface. The drives and their associated computer-readable media
10 provide nonvolatile storage of computer-readable instructions, data structures,
11 program modules and other data for computer 130. Although the exemplary
12 environment described herein employs a hard disk, a removable magnetic disk 148
13 and a removable optical disk 152, it should be appreciated by those skilled in the
14 art that other types of computer-readable media which can store data that is
15 accessible by a computer, such as magnetic cassettes, flash memory cards, digital
16 video disks, random access memories (RAMs), read only memories (ROMs), and
17 the like, may also be used in the exemplary operating environment.

18 A number of program modules may be stored on hard disk 144, magnetic
19 disk 148, optical disk 152, ROM 138, or RAM 140, including an operating system
20 158, one or more application programs 160, other program modules 162, and
21 program data 164. A user may enter commands and information into computer
22 130 through input devices such as a keyboard 166 and a pointing device 168.
23 Other input devices (not shown) may include, but not limited to, a microphone,
24 joystick, game pad, satellite dish, scanner, or the like. These and other input
25 devices are connected to the processing unit 132 through an interface 170 that is

1 coupled to the bus 136. A monitor 172 or other type of display device is also
2 connected to the bus 136 via an interface, such as a video adapter 174. In addition
3 to the monitor, personal computers typically include other peripheral output
4 devices (not shown) such as speakers and printers.

5 Computer 130 commonly operates in a networked environment using
6 logical connections to one or more remote computers, such as a remote computer
7 176. Remote computer 176 may be another personal computer, a server, a router,
8 a network PC, a peer device or other common network node, and typically
9 includes many or all of the elements described above relative to computer 130,
10 although only a memory storage device 178 has been illustrated in Fig. 1. The
11 logical connections depicted in Fig. 1 include a local area network (LAN) 180 and
12 a wide area network (WAN) 182. Such networking environments are
13 commonplace in offices, enterprise-wide computer networks, intranets, and the
14 Internet.

15 When used in a LAN networking environment, computer 130 is connected
16 to the local network 180 through a network interface or adapter 184. When used
17 in a WAN networking environment, computer 130 typically includes a modem 186
18 or other means for establishing communications over wide area network 182, such
19 as the Internet. Modem 186, which may be internal or external, is connected to
20 bus 136 via a serial port interface 156. In a networked environment, program
21 modules depicted relative to personal computer 130, or portions thereof, may be
22 stored in the remote memory storage device. It will be appreciated by one of
23 ordinary skill in the art that the network connections shown herein are exemplary
24 and other means of establishing a communications link between the computers
25 may be used.

1 Generally, the data processors of computer 130 are programmed by means
2 of instructions stored at different times in the various computer-readable storage
3 media of the computer. Programs and operating systems are typically distributed,
4 for example, on floppy disks or CD-ROMs. From there, they are installed or
5 loaded into the secondary memory of a computer. At execution, they are loaded at
6 least partially into the computer's primary electronic memory. The invention
7 described herein includes these and other various types of computer-readable
8 storage media when such media contains instructions or programs for
9 implementing the steps described below in conjunction with a microprocessor or
10 other data processor. The present invention also includes the computer itself when
11 programmed according to the methods and techniques described below.

12 For purposes of illustration, programs and other executable program
13 components such as the operating system are illustrated herein as discrete blocks,
14 although it is recognized that such programs and components reside at various
15 times in different storage components of the computer, and are executed by the
16 data processor(s) of the computer.

17 18 Overview

19 Conventional algorithms that evaluate objects by approximating the object
20 with shapes, e.g. approximating the surface of an object as formed by a plurality of
21 shapes, typically test each individual shape to determine whether a cast ray
22 intersects with a given shape. Exemplary conventional intersection algorithms are
23 described in the following publications: (1) *An Introduction to Ray Tracing*,
24 Andrew Glassner, ISBN 0-12-286160-4, section 3.2, *Polygon Intersection*; (2)
25 *Real-Time Rendering*, Tomas Moller and Eric Haines, ISBN 1-56881-101-2,

1 section 10.5, *Ray/Triangle Intersection*; (3) *Graphics Gems I*, edited by Andrew
2 Glassner, ISBN 0-12-286165-5, pp 390-393, *An Efficient Ray-Polygon*
3 *Intersection*, and p 394, *Fast Ray-Polygon Intersection*; and (4) *Graphics Gems II*,
4 edited by James Arvo, ISBN 0-12-064480-0, pp 257-263, *Ray-Triangle*
5 *Intersection Using Binary Recursive Subdivision*.

6 In the described embodiment, the total number of shapes that are typically
7 evaluated by the conventional algorithms for an intersection are significantly
8 reduced prior to evaluation. This reduction of the number of shapes to be
9 evaluated is achieved by pre-characterizing aspects of the individual shapes that
10 make up an object. Through the illustrated and described pre-characterization
11 processing, a sub-set of possible intersected shapes, which has a smaller number of
12 shapes than the total number of shapes that approximate the surface of the object,
13 is defined, with such sub-set being subsequently evaluated to ascertain those
14 shapes within the sub-set that are intersected by the defined ray. Reducing the
15 number of shapes that are evaluated for ray intersections greatly reduces the
16 processing overhead thereby improving processing times. Improvements over
17 conventional processing techniques have been observed on the order of 5- to 10-
18 times faster.

19 20 **Exemplary Method**

21 Fig. 2 shows a flow diagram that describes processing steps in a ray-
22 intersection method in accordance with the described embodiment. The steps
23 depicted in Fig. 2 can be implemented in any suitable software, hardware, or
24 firmware. Fig. 3, which will be used in connection with the description of Fig. 2,
25 diagrammatically depicts aspects of the processing described by Fig. 2.

1 As shown, a collection of shapes is first defined to approximate an object in
2 connection with a computer graphics program. In this example, the surface of the
3 object is approximated by a collection of shapes. Fig. 3 shows an exemplary
4 portion of such a collection generally at 300. Any suitable shapes can be used. In
5 the described embodiment, the shapes have a similar geometry. Typically,
6 polygons having a plurality of vertices are used. As will become apparent below,
7 it is advantageous to select polygons that collectively have more faces than
8 vertices when approximating the surface of an object. In the illustrated example,
9 the polygons comprise triangles.

10 As shown in Fig. 3, there are four depicted triangles, 302, 304, 306, and
11 308 having the vertices V_1 - V_7 as indicated. There can be many thousands of
12 triangles used to approximate the surface of an object. Additionally, the collection
13 of triangles can be arranged to have different topologies. Exemplary topologies
14 are shown in Figs. 4-6. Specifically, Fig. 4 shows a topology known as a triangle
15 mesh; Fig. 5 shows a topology known as a triangle strip; and Fig. 6 shows a
16 topology known as a triangle fan. The collection of triangles can be arranged so
17 that some of them share a side and/or vertices. For example, in Fig. 3, triangles
18 302 and 304 share a vertex, while triangles 304 and 306 share a side and two
19 vertices. Other collections can be defined where none of the triangles share a
20 vertex. Although triangles are depicted in the illustrated and described
21 embodiment, it is to be understood that other shapes or polygons can be used to
22 approximate an object.

23 Step 200 (Fig. 2) defines a ray (Fig. 3) that is cast toward the approximated
24 object. The ray can be cast in any suitable manner using conventional ray-
25 intersection techniques. Here, a ray (Fig. 3) is seen to extend toward and through

1 collection 300. Step 210 defines at least one reference object relative to the
2 approximated object. The reference object is defined in such a way that it contains
3 at least a portion of the ray. In the illustrated and described embodiment, the
4 reference object comprises a plane (Fig. 3), and the plane contains the entirety of
5 the ray. In the described embodiment, a plane that is parallel to one of the x, y, and
6 z axes is selected to reduce the processing complexity as will be described just
7 below.

8 Specifically, a plane can be represented by the following equation:

$$Ax + By + Cz + D = 0$$

11 Here, x, y, and z represent some point for which there is a real value. The
12 real value for the point increases positively as the point is moved in a positive
13 direction away from the plane. Similarly, the real value for the point increases
14 negatively as the point is moved in a negative direction away from the plane.

15 In the described embodiment, by selecting the plane such that it is parallel
16 to one of the above-mentioned axes (here, the z axis), $z=0$ and the equation is
17 simplified as follows:

$$Ax + By + D = 0$$

20 This optimization results in one less addition and one less multiply
21 operation, which reduces the processing complexity.

22 Step 212 pre-characterizes aspects of the individual shapes that comprise
23 the collection of shapes to provide characteristic data. In the illustrated and
24 described embodiment, pre-characterization identifies positional aspects of sub-
25 components of the individual shapes. The sub-components in this example

comprise the vertices of the triangles. Specifically, the position of each vertex is computed relative to the plane by simply evaluating the equation immediately above. The following table sets forth the outcome of the evaluation for each vertex:

Equation Result	Position Relative To Plane
$Ax + By + D = 0$	On the plane.
$Ax + By + D < 0$	Negative side of the plane.
$Ax + By + D > 0$	Positive side of the plane.

Here, depending on the coordinates of the particular vertex, the evaluation of the equation will ascertain where the vertex is positioned relative to the plane. That is, if the equation evaluates to zero, then the vertex is on the plane. Alternatively, if the equation evaluates to less than or greater than zero, the vertex is on the negative side or positive side of the plane, respectively. In the Fig. 3 example, evaluation of the illustrated vertices will give the following results set forth in the table below:

Equation Result	Position Relative To Plane
$V_1 < 0$	Negative side of plane.
$V_2 < 0$	Negative side of plane.
$V_3 = 0$	On the plane.
$V_4 > 0$	Positive side of plane.
$V_5 > 0$	Positive side of plane.

$V_6 > 0$	Positive side of plane.
$V_7 > 0$	Positive side of plane.

This evaluation is set forth in pseudo code as follows:

```

Boolean    vflags [nVertices]
For i in [0, nVertices)
    If (A * vertices [i]x + B * vertices [i]y + D) > 0
        Vflag[i] ← true
    Else
        Vflag[i] ← false

```

Essentially, this routine loops through each vertex flagging it as “true” if it is on the positive side of the plane, or flagging it as false if it is on the negative side of the plane. For purposes of the analysis and evaluation of the shapes discussed below, a point that is on the plane can be considered as being either on the positive or the negative side of the plane, as long as consistency is maintained throughout the evaluation.

With the pre-characterization processing having been done as described above, step 214 uses the characteristic data to ascertain the position of a shape relative to the reference object. This step determines whether or not a particular individual shape has a chance of being intersected by the ray. In this manner, a sub-set of shapes that might be intersected by the ray is defined by determining which of the shapes satisfies a predefined relationship relative to the reference object, i.e. plane. Using the above example, the following is ascertained. If a shape’s vertices are all on one side or the other of the reference object, here the Fig. 3 plane, then it is impossible for the ray (which lies in the plane) to intersect the shape. Accordingly, these shapes can be immediately discarded. If, on the

other hand, a shape's vertices lie on both sides of the plane, then the object is said to "straddle" the plane. If this is the case, then the shape might be, but is not necessarily, intersected by the ray (e.g. the shape might straddle the plane, but be located entirely above or below the ray). This evaluation is set forth as steps 214 through 220. There, step 214 uses the characteristic data to ascertain the position of a shape relative to the reference object. This step determines whether a shape is on the negative side of, positive side of, or straddles the plane. Step 216 determines whether a particular shape meets position criteria. In this example, the position criteria assists in defining a sub-set of shapes that straddle the plane. If position criteria is met, then step 218 adds the shape to a list of shapes that are to be evaluated for intersection. If the position criteria is not met, or if the shape was added to the list of shapes for evaluation, step 220 then determines whether there are any additional shapes to evaluate. If there are, then the method loops back to step 216 and evaluates the next shape. If there are no additional shapes to evaluate, then step 222 evaluates the shapes that are on the list to ascertain whether the cast ray intersects one or more of the shapes. The intersection processing of step 222 can take place through the use of conventionally known techniques.

As an example, consider Fig. 3 again and assume that the adopted convention will consider vertices that lie on the plane to be on the positive side of the plane. Evaluation of all of the vertices indicates that only triangle 302 "straddles" the plane. In this case, the position criteria (i.e. does the shape straddle the plane?) is met. Hence, triangle 302 is added to the list of shapes that is to be evaluated (step 218) and step 220 determines whether there are any additional shapes to evaluate. Processing then takes place as described above.

1 Exemplary pseudo code that sets forth one way of accomplishing this task
2 is set forth as follows:

```
3
4   For i in [0, nTriangles)
5       If (Vflag[tri[i][0] = Vflag[tri[i][1]])
6           And
7           (Vflag[tri[i][1] = Vflag[tri[i][2]])
8               Skip to next i
9
10          If StandardIntersect (tri[i][0], tri[i][1], tri[i][2])
11              Return TRUE
```

8 What this code does is loop through each of the flags for the vertices of a
9 shape to determine whether the vertices are all equal. For example, if all of the
10 flags for a vertex are true or false, then the shape can be discarded and the code
11 skips to the next *i* or shape. If, on the other hand, some of the flags are true and
12 other of the flags are false, then the code executes a "StandardIntersect" algorithm,
13 which determines whether or not there is an intersection. If there is an intersection
14 between the ray and the shape, then the code returns a value indicating that this is
15 the case. In this case, advantageously, the inventive processing rules out many of
16 the shapes prior to using an intersect algorithm to ascertain whether or not there is
17 an intersection between the ray and one or more shapes.

18 Example

19 As a further example, consider Figs. 7-10. Fig. 7 shows a collection of
20 shapes 400 that comprise a triangle mesh approximating an object of interest.
21 Although this example is described in the context of a triangle mesh, it will be
22 apparent to those of skill in the art, that the evaluation described just below can be
23 conducted in connection with other topologies, examples of which are given
24 above. In this particular example, the collection constitutes 60 surfaces (each
25

triangle comprising one surface) and 42 vertices. In the past, intersection algorithms have evaluated each of the separate triangles of the illustrated collection to determine whether there is an intersection with a cast ray. So, in this case, conventional methods might have started with the first triangle in the first column, evaluated it for an intersection, and then discarded it when there was no intersection. This method would then step through each of the triangles, similarly evaluating them for an intersection with the ray. With complex surfaces having a high degree of resolution (i.e. many shapes), processing overhead can be quite large. Advantageously, the described embodiment reduces the number of shapes that must be tested for an intersection. This saves greatly on processing overhead and increases the speed with which objects are processed.

Fig. 7 shows a ray that has been cast toward the object that is approximated by collection 400. The ray extends into and out of the plane of the page upon which Fig. 7 appears. Fig. 8 shows a plane containing the ray that is perpendicular to the page upon which Figures 7 and 8 appear. Fig. 9 shows a sub-set of shapes (shaded for clarity) that might be intersected by the ray. Here, an evaluation has been performed to determine whether the triangle(s) that are defined by the individual vertices straddle the defined plane. If they do straddle the defined plane, then it is possible that they are intersected by the ray. Here, the number of triangles that have to be evaluated by an intersection algorithm have been reduced from 60 to 10.

Fig. 10 shows collection 400 after the intersection algorithm has been run on all of the triangles in the shaded sub-set of Fig. 9. In this example, only one triangle (shaded for clarity) is intersected by the ray.

Multiple Plane Embodiment

In another embodiment, multiple planes are used to further reduce the subset of shapes that need to be evaluated by an intersection algorithm.

Fig. 11 shows collection 400 at a processing point that is the same shown in Fig. 9. Fig. 11, however, illustrates a second plane defined to contain the ray and perpendicular to the first plane. It should be understood that the second plane does not have to be perpendicular to the first plane, but may in fact intersect the first plane in any direction orthogonal to the first plane. At this point, the shapes have been evaluated to determine whether they straddle the first plane. This has produced the illustrated shaded sub-set. Fig. 12 shows an exemplary subset after the shapes of the Fig. 11 sub-set have been evaluated to determine whether they straddle the second plane. In this case, the triangles that need to be evaluated by the intersection algorithm have been reduced from 60 to two. Of course, as should be apparent to one of ordinary skill in the art, more than two reference objects may be used for purposes of further paring down the number of shapes that need to be evaluated.

The inventive methods and apparatus greatly facilitate computer graphics processing by reducing processing complexities associated with ray-intersection. Advancements are achieved by reducing the number of shapes in a collection that must be evaluated for ray intersection. The described embodiment achieves its processing advances by recognizing that aspects of the shapes can be pre-processed prior to subjecting them to intersection processing. The selected aspects in the described embodiment are the vertices of the polygons that comprise the collection. By pre-characterizing the vertices of the polygons, certain polygons are ruled out before they are processed by an intersection algorithm.

1 Although the invention has been described in language specific to structural
2 features and/or methodological steps, it is to be understood that the invention
3 defined in the appended claims is not necessarily limited to the specific features or
4 steps described. Rather, the specific features and steps are disclosed as preferred
5 forms of implementing the claimed invention.

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